Substitute (	or form 1449A/PTC	)		Complete if Known		
INFO	MATION	חופרו מ	SUDE	Application Number	10/676,883	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	9/30/2003	
				First Named Inventor	Christopher L. Rumer	
				Art Unit	2825	
Sheet	1	of	<del></del>	Examiner Name	C. Everhant	
			1	Attorney Docket Number	42P16840	

NON PATENT LITERATURE DOCUMENTS								
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the erticle (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T'					
CME		RECHE, J., "Wafer Thinning and Thru-Silicon Vias", The Path to Wafer Level Packaging, 41 pages, Tru-Si Technologies, www.trusi.com, IEEE/CPMT Meeting, Santa Clara (CA)						
		05/10/2000.						
<u>_</u> .								

Examiner Signature	C. Roeshart	Date Considered	11-12-2004	

<sup>\*</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>&#</sup>x27;Applicant's unique citation designation number. 'Applicant is to place a check mark here it English language Translation is attached.